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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/729.491	12/04/2003	1765	770	100-14320 (P04927-D01)	20	12	1

33402 LAW OFFICES OF MARK C. PICKERING P.O. BOX 300 PETALUMA, CA 94953 CONFIRMATION NO. 3061
CORRECTED FILING RECEIPT
\*OC000000012592602\*

Date Mailed: 05/11/2004

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Applicant(s)

Terry Lines, Crowley, TX;

### Domestic Priority data as claimed by applicant

This application is a DIV of 09/824,653 04/03/2001 PAT 6,703,670

Foreign Applications

If Required, Foreign Filing License Granted: 03/08/2004

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

**Early Publication Request: No** 

Title

Method for forming a depletion-mode transistor that eliminates the need to separately set the threshold voltage of the depletion-mode transistor

**Preliminary Class** 

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